INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
IDTL002US1-1826	~~TBA~~ 10/621,055
Applicant(s) Talledo, Cesar A. et al.	
Filing Date July 15, 2003	**Group Art Unit** ~~TBA~~ 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER *(Including Author, Title, Date, Pertinent Pages, Etc.)*

21		Ryan, Kevin, "DDR SDRAM Functionality and Controller Read Data Capture," DesignLine, Micron Technology, Inc., Volume 8, Issue 3, 3Q99.

EXAMINER	DATE CONSIDERED
	11/15/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.